

CUSTOMER NO.
34456

AMENDMENTS TO THE SPECIFICATION:

Please amend paragraph [0022] as follows:

[0022] Thus buffer 40 provides low input impedance and low conductive losses. Buffer 40 uses the inherent parasitic capacitance of MOS transistors in conjunction with another capacitor to form a capacitive divider to reduce the voltage swing of an AC signal presented to the MOS transistors. This voltage swing reduction allows the use of fast, low-voltage transistors having relatively-thin gate oxide without stressing the gate oxide. If the peak-to-peak voltage swing on the input signal is equal to a first voltage then, in one embodiment, these low-voltage transistors can have an oxide stress voltage that is less than the first voltage.